

Notice of References Cited

Application/Control No.

09/683,243

Applicant(s)/Patent Under
Reexamination
YU ET AL.

Examiner

Albert Wang

Art Unit

2115

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,732,266	05-2004	Anderson et al.	713/100
	B	US-6,697,952	02-2004	King, Joseph D.	713/300
	C	US-6,691,235	02-2004	Garcia et al.	713/300
	D	US-6,601,179	07-2003	Jackson et al.	713/322
	E	US-6,035,407	03-2000	Gebara et al.	713/300
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Philips Semiconductors, "PCA9559 5-Bit Multiplexed/1-Bit Latched 6-Bit I2C EEPROM", Data Sheet, January 2000.
	W	California Micro Devices, "CMBUS100 - AMD Athlon VID/FID Interface", Data Sheet, March 2002.
	X	Attansic Technology, "ATXP1 - Jumper Free Over Clock Controller", Data Sheet, Revision 1.01, September 2001.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.